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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Appl. No.: 10/604, 277

Applicant(s): Bednar et al.

Filed.: July 8, 2003

Art Unit: 2825

Dkt. No.: BUR920020092US1

Examiner: TAT, BINH C.

Title: NESTED VOLTAGE ISLAND ARCHITECTURE

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Honorable Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE TO A NOTICE OF NON-COMPLIANT AMENDMENT**

This paper is filed in response to a Notice of Non-Compliant Amendment mailed on 10/24/2005 in response to a Request for Reconsideration filed in response to the Office Action mailed on July 28, 2005. As directed, this paper only includes a corrected "Amendments to the claims" section.

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